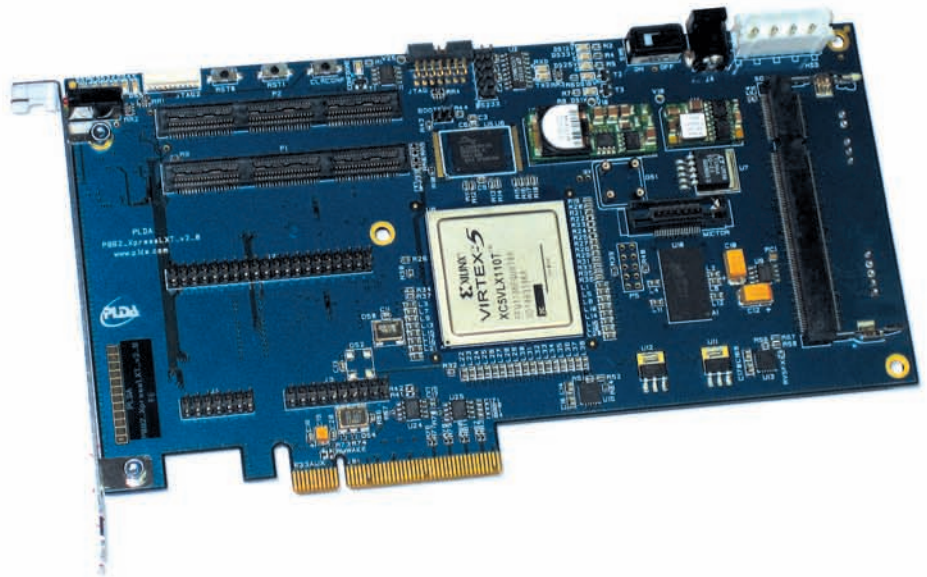


XPRESSLXT

PROTOTYPING BOARD FOR PCIe



Prototype a PCIe system (up to 8lanes) with a Xilinx Virtex-5 LXT, SXT or FXT



Applications

- PCIe x8 Endpoint
- PCIe x8 Rootport
- PCIe Switch
- PCIe cabling (upstream & downstream)
- ASIC Prototyping
- PCIe to SFP connectors
- Data Acquisition

Benefits

Secure & Proven Solution:

- PCI-SIG compliant product
- Includes PCI-SIG Compliant IP Core for PCIe
- Extensive Validation testing

Minimize time to market:

- Includes EZDMA IP (DMA Channels controller IP)
- Includes FREE PLDA PCIe Testbench
- Support directly from the IP design team
- Immediate design and testing of Endpoint and Rootport projects

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PCI EXPRESS®

> Features

- x8 PCIe male connector
- x1, x4, x8 Endpoint and Rootport IP controller for PCI Express
- Xilinx Virtex 5 supported:
 - Virtex 5 LX110T
 - Virtex 5 LX155T
 - Virtex 5 SX95T
- Other devices of the Virtex 5 family will be supported
- PCI Express 2.0 compliant
- 128 MB on-board DDR2. Up to 2GB DDR2 on SODIMM connector
- FPGA programmable via a PCIe interface
- Extension interfaces for daughterboards including:
 - 13 LVDS RX/TX
 - 8 Gb RX/TX
 - 100 LVCMOS25
 - 45 LVCMOS33
- Can be used in stand-alone mode with external power supply
- Optional daughterboards available:
 - **Rootport daughterboard:** one x8 female connector (Ordering Code RP_HSI v1.1)
 - **Breadboard daughterboard:** large prototyping area (Ordering Code BB_HSI v1.0)
 - **Switch daughterboard:** one x4 female connector, two x1 female connector and x1 cabling connector (Ordering Code SW_HSI v1.1)
 - **SFP daughterboard:** 3 SFP connectors available on daughterboard & 1 Gigabit link on SMA connector (Ordering Code SFP_HSI)
 - **Camera Link:** interface for vision application (Ordering Code KLINK_BASE)
 - **PCIe x1 and x4 cabling:** (Ordering Code PCIEC4_D, PCIEC4_U)



> Deliverables

- XpressLXT Prototyping Board
- SFP_HSI daughter board
- RP_HSI if required
- Technical support and free IP update
- PCIe x1, x4, x8 Xpress Lite IP - (licensed for board use)
- XpressFlash PCIe software
- Complete documentation :
 - XpressLXT Reference Manual
 - Getting Started Guide
 - Schematics
 - XpressLXT Build history

PLDA (PLDA - www.plda.com) designs and sells a wide range of FPGA and ASIC interfacing solutions for the PCI and derivative markets (PCI Express, PCI-X, CompactPCI, and PC104/PC104+).

We provide complete solutions to a global market, including IP cores, hardware, software, consulting services, and comprehensive technical support provided directly by the IP designers.

Our success is driven by our expertise in designing Intellectual Property Cores and related boards for ASICs, Structured-ASICs, and FPGAs for high-speed buses, including: PCI Express, PCI-X, PCI, PXI, AHB-PCI.

Founded in 1996, PLDA is headquartered in Aix-en-Provence, France and has offices in San Jose, California. Its products are available worldwide through a strong distribution network.



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